

## **Dr. K. Renganathan, M.Tech, PhD**



**Dr. K. Renganathan** was born on 27<sup>th</sup> July, 1980 at Chennai, Tamilnadu, India. His academic background is totally in the fields of Instrumentation, Control and Electronics Engineering and holds an M.Tech Degree in Power Electronics and PhD degree in the field of Fault Diagnosis and Fault Tolerance control of systems.

He has published 12 International Journal Papers till date in renowned International Journals and more than 15 International Conference Proceedings, 2 National journal papers publications and 1 patent publication to his credit. He is also a regular reviewer of International Journal of ISA Transactions of Elsevier publications, IEEE Systems, Man and Cybernetics Journal and SAP Journal, USA. He is an active ISA member and also member of IEEE, IETE and ACDOS. He is Joint supervisor for two PhD candidates and PhD doctoral committee member for 4 PhD candidates.

He is currently Professor and Head in the Department of Electronics and Instrumentation, Sri Sairam Engineering College, Chennai, Tamilnadu, India. He has a total of 16.5 years of Experience in teaching field. He is also the Innovation Activity Coordinator of the MHRD Institutions Innovation Council (IIC) of the Institution. He was also instrumental in developing the Innovation and Entrepreneurship Centre (IEDC) in the Institution and has successfully guided and motivated many students to develop socially relevant and marketable innovative products (like low cost eco-friendly mobile toilets, foldable dining table, fake currency detector etc) which are currently useful for the society. Through his sustained efforts and continuous dedication the Institution recently got the most prestigious ARIIA ranking wherein the Institution was ranked and categorized as Band-A Institution (rank between 6-25) in the category of “Private or Self-Financed College/Institute” and the Institution was also given 5-star status by MHRD IIC for the academic year 2019-2020.